Status report OpenCL setup, board test

Contents

[Platform 2](#_Toc486105143)

[System 2](#_Toc486105144)

[Quartus stack 2](#_Toc486105145)

[Prequisites 2](#_Toc486105146)

[Compilation (board test) 3](#_Toc486105147)

[Board preparation 4](#_Toc486105148)

[Deployment 5](#_Toc486105149)

[Execution 6](#_Toc486105150)

# Platform

## System

* Core i7 6500u, 8GB RAM
* Windows 10 Pro x64
* Windows Subsystem for Linux
* PuTTY

## Quartus stack

* Quartus 2 14.1.0.186
* AOCL 14.1.0.186
* SoC EDS 14.1.0.186

# Prequisites

1. Setup Altera Quartus stack
2. Setup USER[[1]](#footnote-1) environment variables
   1. Altera OpenCL License (Create LM\_LICENSE\_FILE)
   2. Altera OpenCL binaries (Add to PATH)
   3. Altera Board package path (AOCL\_BOARD\_PACKAGE\_ROOT)

# Compilation (board test)

1. Go to “C:\altera\14.0\hld\board\terasic\de1soc\examples\boardtest”
2. Prompt> aoc device/boardtest.cl --sw-dimm-partition -o bin/boardtest.aocx
3. Open “C:\altera\14.1\embedded\Embedded\_Command\_Shell.bat” embedded shell
   1. A Cygwin [[2]](#footnote-2)shell opens [https://www.cygwin.com/]
   2. Go to /cygdrive/c/altera/14.1/hld/board/terasic/de1soc/examples/boardtest[[3]](#footnote-3)
   3. Build the board test project (See image x)

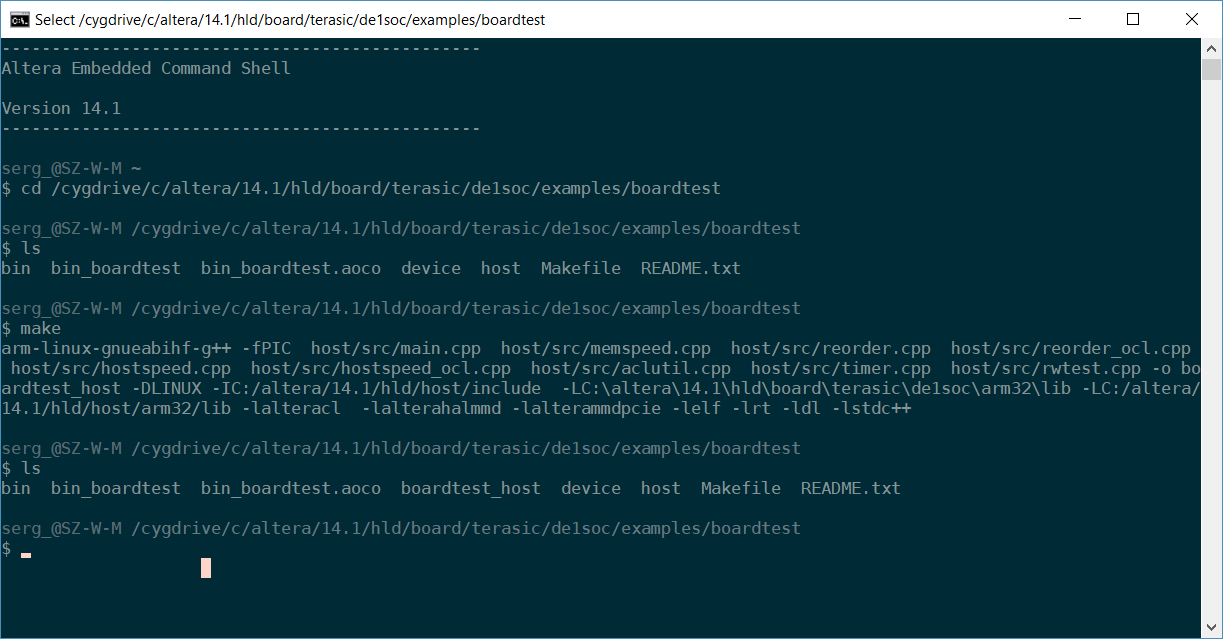


Figure Altera Embedded Command Shell

# Board preparation

Prior to deployment the DE1-SoC has to be up and running.

A prepared flash card with a linux base system is required.

1. Connect the power, USB/UART and Ethernet connectors.
2. Start PuTTY, select the proper COM port and set the baudrate to *115200*
3. Log in as “root”. A password is not required.
4. Set up a (static) ip address, e.g *192.168.0.123*

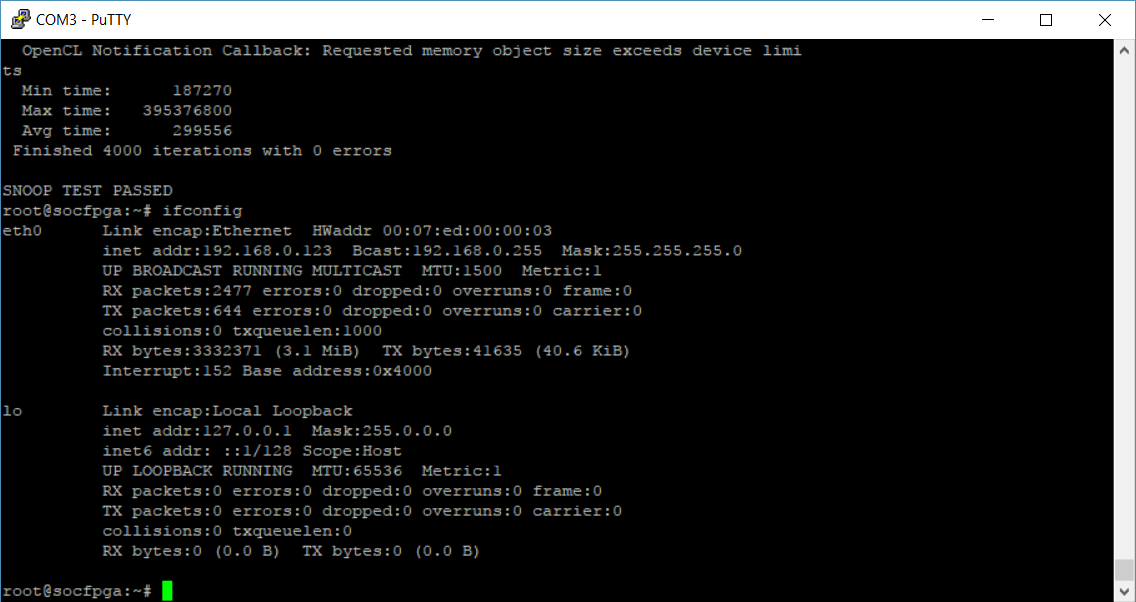


Figure Board's IP configuration

# Deployment

From a terminal:

* Copy ACL device configuration: *scp bin/boardtest.aocx* [*root@192.168.0.123:/home/root*](mailto:root@192.168.0.123:/home/root)
* Copy OpenCL host application: *scp boardtest\_host* [*root@192.168.0.123:/home/root*](mailto:root@192.168.0.123:/home/root)

These commandos copy files to the target board via SSH[[4]](#footnote-4).

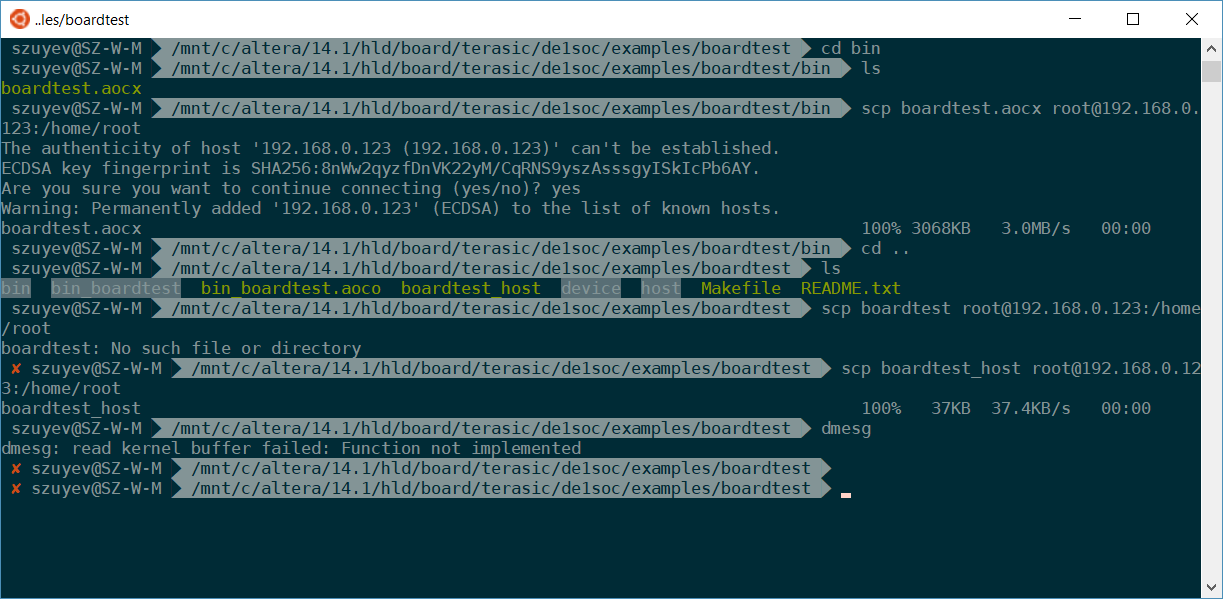


Figure Copy necessary files to the board

# Execution

* Make boardtest\_host executable: *chmod +x ./boardtest\_host*
* Prepare the OpenCL environment*: source ./init\_opencl.sh*
* Program the device: *aocl program /dev/acl0 boardtest.aocx*
* Execute the OpenCL host application: *./boardtest\_host*

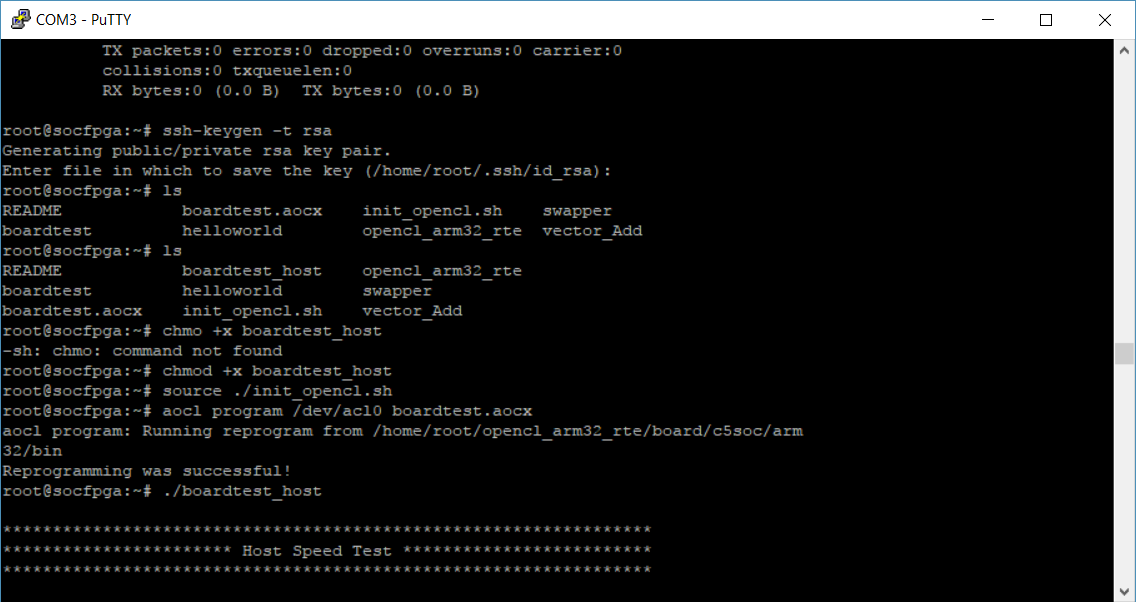


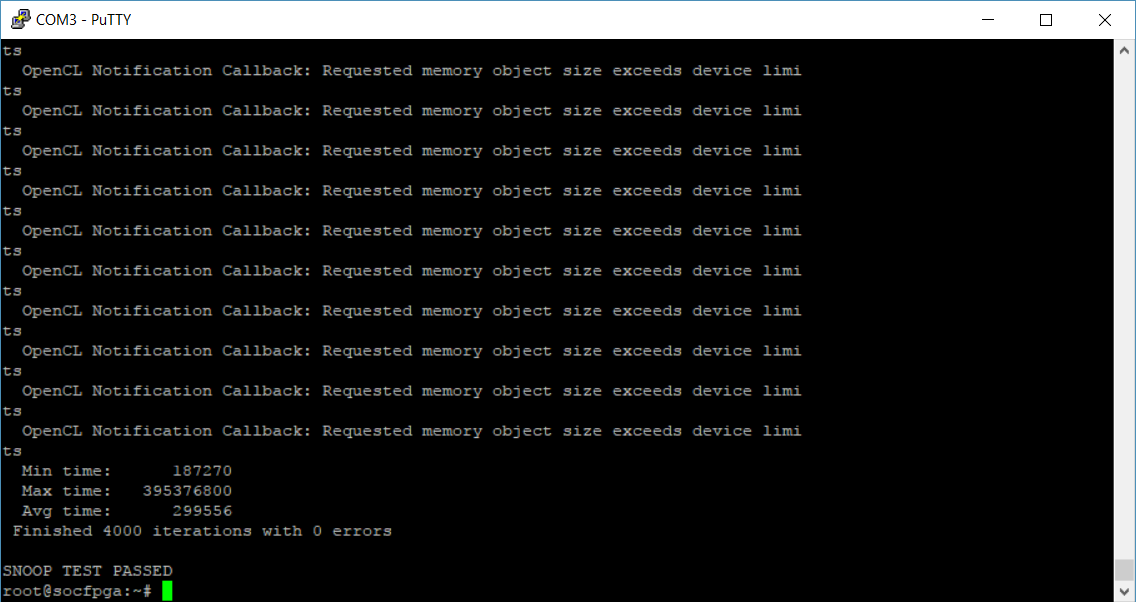
Figure Steps prior to execute the OpenCL program on the board

Figure OpenCL board test passed

1. Systemwide variables won’t work without adapting Altera’s own environment variables [↑](#footnote-ref-1)
2. POSIX emulation layer + tools for windows, https://www.cygwin.com/ [↑](#footnote-ref-2)
3. To access a host path via Cygwin shell /cygdrive “prefix” is required (mountpoint) [↑](#footnote-ref-3)
4. Secure Shell [↑](#footnote-ref-4)